

## Amendments to the Claims:

This listing of claims will replace all prior versions, and listings, of claims in the application.

1. (Currently Amended) A method comprising:

a processor executing a BIOS routine by receiving information from at least ~~one~~ a first computing system unit[[s]], the information ~~comprises~~ comprising at least one of error information, status information, and configuration information;

the processor executing the BIOS routine by storing ~~said~~ the received information in a memory;

~~for each of at least one~~ a second computing system unit[[s]],

the processor executing the BIOS routine by receiving an initial request for ~~said~~ the received information from the second computing system unit;

the processor executing the BIOS routine by providing to the second computing system unit in response to ~~said~~ the received request at least one of ~~said~~ the received information stored in ~~said~~ the memory before the receipt of ~~said~~ the request if any is stored; and

the processor executing the BIOS routine by providing to the second computing system unit at least one of ~~said~~ the received information received subsequent to ~~said~~ the request.

2. (Currently Amended) The method defined in claim 1 wherein ~~said~~ the information comprises at least one of unit information, system information, error information, status information, configuration information, and event information.

3. (Currently Amended) The method defined in claim 1 wherein the receiving ~~said~~ the information from the first unit[[s]] is according to an API.

4. (Currently Amended) The method defined in claim 1 wherein the providing ~~said~~ the information to the second unit[[s]] is according to an API.
5. (Currently Amended) The method defined in claim 1 wherein the storing the received information in a memory is according to a time of receipt of ~~said~~ the information.
6. (Currently Amended) The method defined in claim 1 wherein the providing at least one of the received information stored in the memory before a receipt of ~~said~~ the request is according to a time of receipt by ~~said~~ the processor.
7. (Currently Amended) The method defined in claim 1 wherein ~~said~~ the information received by ~~said~~ the processor is appended to include at least one of a sequence number and an absolute time.
8. (Currently Amended) The method defined in claim 1 wherein the provided received information stored before the receipt of ~~said~~ the request includes one of all ~~said~~ the received information and a categorized subset of all ~~said~~ the received information.
9. (Currently Amended) The method defined in claim 1 wherein the provided information received subsequent to ~~said~~ the request includes one of all ~~said~~ the received information and a categorized subset of all ~~said~~ the received information
10. (Currently Amended) A machine-readable medium that provides instructions, which when executed by a processor, cause said processor to perform operations comprising:
  - during execution of a BIOS routine, receiving information from at least one first computing system unit[[s]], the information ~~comprises~~ comprising at least one of error information, status information, and configuration information;

during execution of the BIOS routine, storing ~~said~~ the received information in a memory;

~~for each of at least one~~ a second computing system unit[[s]],

during execution of the BIOS routine, receiving an initial request for ~~said~~ the received information from the second computing system unit;

during execution of the BIOS routine in response to ~~said~~ the request, providing to the second computing system unit at least one of ~~said~~ the information stored in ~~said~~ the memory before the receipt of ~~said~~ the request if any is stored; and

during execution of the BIOS routine, providing to ~~said~~ the second computing system unit at least one of ~~said~~ the received information received subsequent to ~~said~~ the request.

11. (Currently Amended) The medium defined in claim 10 wherein ~~said~~ the information comprises at least one of unit information, system information, error information, status information, configuration information, and event information.

12. (Original) The medium defined in claim 10 wherein the receiving information is according to an API.

13. (Currently Amended) The medium defined in claim 10 wherein the providing ~~said~~ the information stored to the second units is according to an API.

14. (Currently Amended) The medium defined in claim 10 wherein the storing the received information in a memory is according to a time of receipt of ~~said~~ the information.

15. (Currently Amended) The medium defined in claim 10 wherein the providing the received information stored in the memory before a receipt of ~~said~~ the request is according to a time of receipt by ~~said~~ the processor.

16. (Original) The medium defined in claim 10 wherein the information received is appended to include at least one of a sequence number and an absolute time.

17. (Currently Amended) The medium defined in claim 10 wherein the provided information stored before the receipt of ~~said~~ the request includes one of all ~~said~~ the received information and a categorized subset of all ~~said~~ the received information

18. (Currently Amended) The medium defined in claim 10 wherein the provided information received subsequent to ~~said~~ the request includes one of all ~~said~~ the received information and a categorized subset of all ~~said~~ the received information

19. (Currently Amended) A computer that comprises:  
a stored BIOS program in a non-volatile memory that includes instructions that cause ~~said~~ the computer to:

receive information from ~~at least one~~ a first unit[[s]] coupled to ~~said~~ the computer, the information ~~comprises~~ comprising at least one of error information, status information, and configuration information;

store ~~said~~ the received information in a memory;

for ~~each of at least one~~ a second unit[[s]] coupled to ~~said~~ the computer,

receive an initial request for ~~said~~ the information from the second unit;

in response to ~~said~~ the request, provide to the second unit at least one of ~~said~~ the information stored in ~~said~~ the memory before the receipt of ~~said~~ the request if any is stored, and

provide to the second unit at least one of ~~said~~ the received information to the second unit received subsequent to ~~said~~ the request.

20. (Original) The computer defined in claim 19 where the information includes at least one of unit information, system information, error information, status information, configuration information, and event information.

21. (Original) The computer defined in claim 19 wherein the receive information from the first units is according to an API.

22 (Original) The computer defined in claim 19 wherein the provide to the second unit information is according to an API.

23. (Currently Amended) The computer defined in claim 19 wherein the store the received information in a memory is according to a time or receipt of ~~said~~ the information.

24. (Currently Amended) The computer defined in claim 19 wherein the provide the received information stored in the memory before a receipt of ~~said~~ the request is according to a time of receipt of ~~said~~ the information.

25. (Currently Amended) The computer defined in claim 19 wherein the information received by ~~said~~ the processor is appended to include at least one of a sequence number and an absolute time.

26. (Currently Amended) The computer defined in claim 19 wherein the provided information stored before the receipt of ~~said~~ the request includes one of all ~~said~~ the received information and a categorized subset of all ~~said~~ the received information.

27. (Currently Amended) The computer defined in claim 19 wherein the provided information received subsequent to ~~said~~ the request includes one of all ~~said~~ the received information and a categorized subset of all ~~said~~ the received information.